

INFORMATION DISCLOSURE CITATION

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Applicants Mie MATSUO et al.	
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U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
<i>SM</i>	6,624,506 B2	09/23/03	SASAKI et al.	<i>1</i>	<i>1</i>	
	6,614,106 B2	09/02/03	MATSUO et al.			

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	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
<i>SM</i>	2002-110865	04/12/02	JAPAN	<i>1</i>	<i>1</i>	ABSTRACT
<i>SM</i>	2001-102479	04/13/01	JAPAN			ABSTRACT
<i>SM</i>	2760188	03/20/98	JAPAN			ABSTRACT
<i>SM</i>	2000-349229	12/15/00	JAPAN			ABSTRACT
<i>SM</i>	05-283606	10/29/93	JAPAN			ABSTRACT

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
	YODA, T. et al., "Semiconductor Integrated Circuit Device Having Interposer And Method of Manufacturing The Same", U.S. Patent Application No. 09/669,724, filed on September 26, 2000.

Examiner <i>SM</i>	Date Considered <i>10/673192</i>
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
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